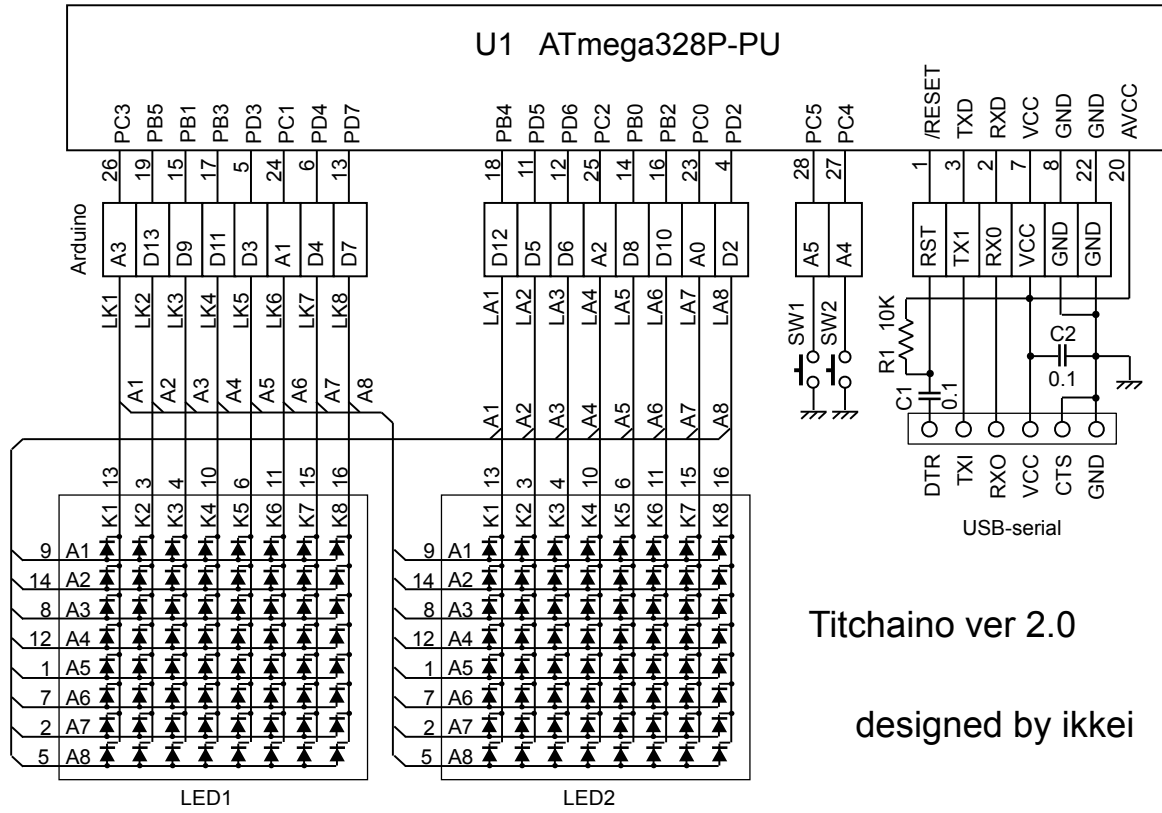


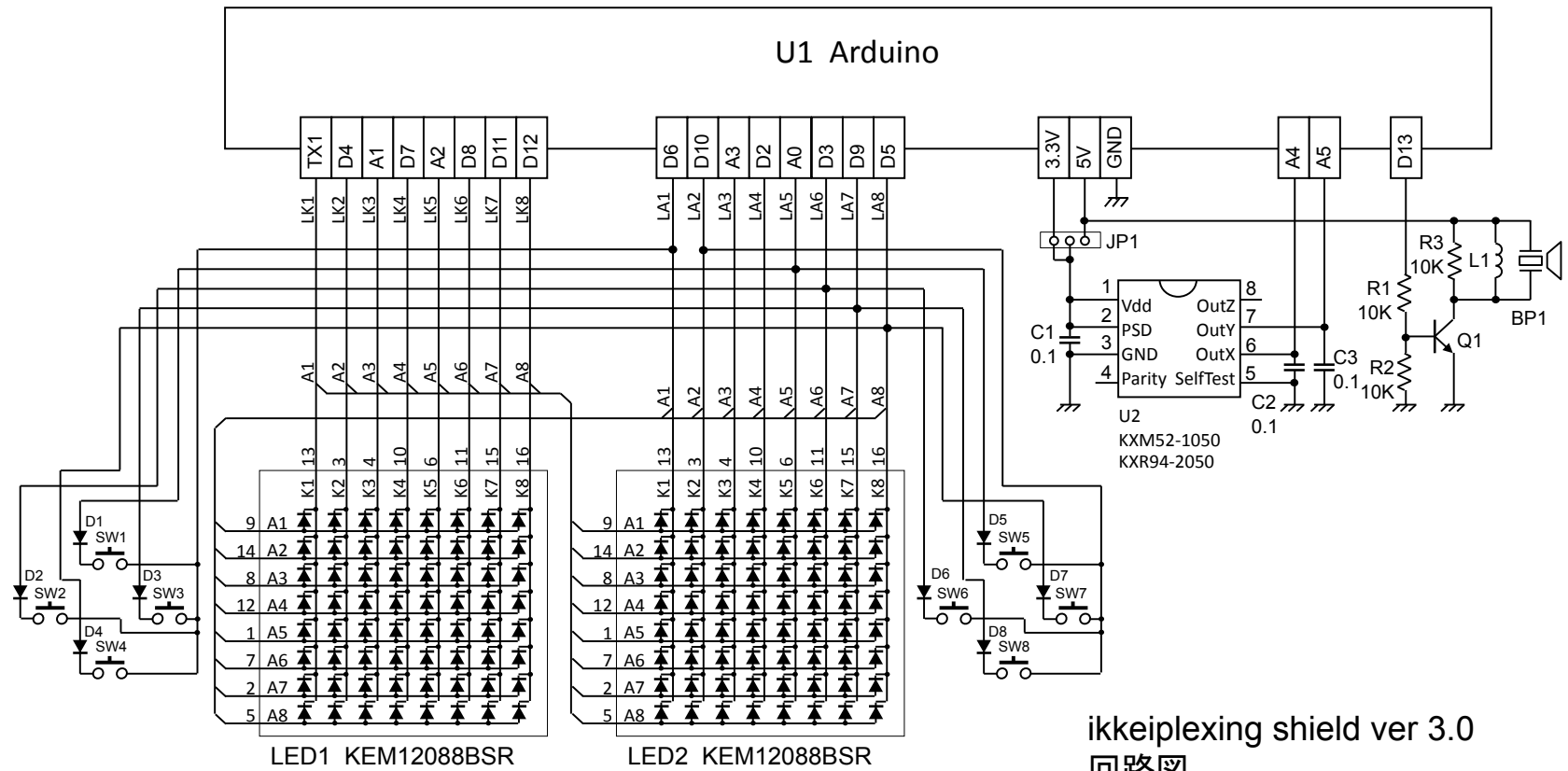
Titchaino ver 2.0



Titchaino ver 2.0

designed by ikkei

ikkei-plexing shield ver 3.0 回路図



ikkei-plexing shield ver 3.0
回路図

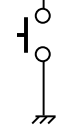
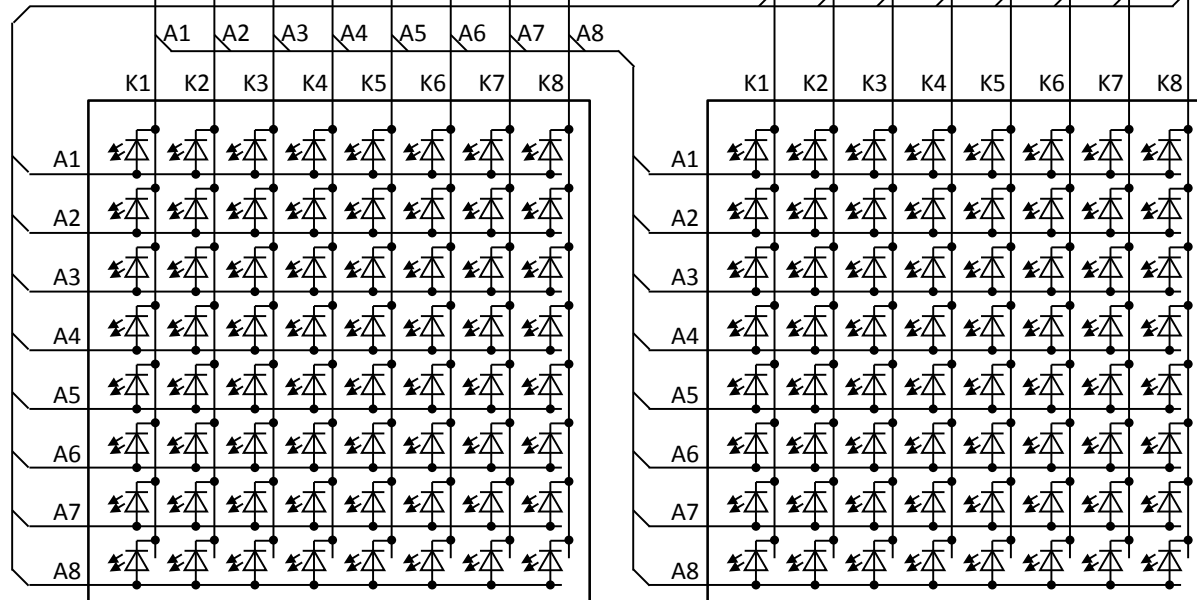
ATmega328P

A3 D13 D9 D11 D3 A1 D4 D7
PC3 PB5 PB1 PB3 PD3 PC1 PD4 PD7

D12 D5 D6 A2 D8 D10 A0 D2
PB4 PD5 PD6 PC2 PB0 PB2 PC0 PD2

26	19	15	17	5	24	6	13
LK1	LK2	LK3	LK4	LK5	LK6	LK7	LK8

18	11	12	25	14	16	23	4
LA1	LA2	LA3	LA4	LA5	LA6	LA7	LA8



ATmega328P

A3 D13 D9 D11 D3 A1 D4 D7 D12 D5 D6 A2 D8 D10 A0 D2
PC3 PB5 PB1 PB3 PD3 PC1 PD4 PD7 PB4 PD5 PD6 PC2 PB0 PB2 PC0 PD2

26 19 15 17 5 24 6 13 18 11 12 25 14 16 23 4

LK1 LK2 LK3 LK4 LK5 LK6 LK7 LK8 LA1 LA2 LA3 LA4 LA5 LA6 LA7 LA8

A1 A2 A3 A4 A5 A6 A7 A8

