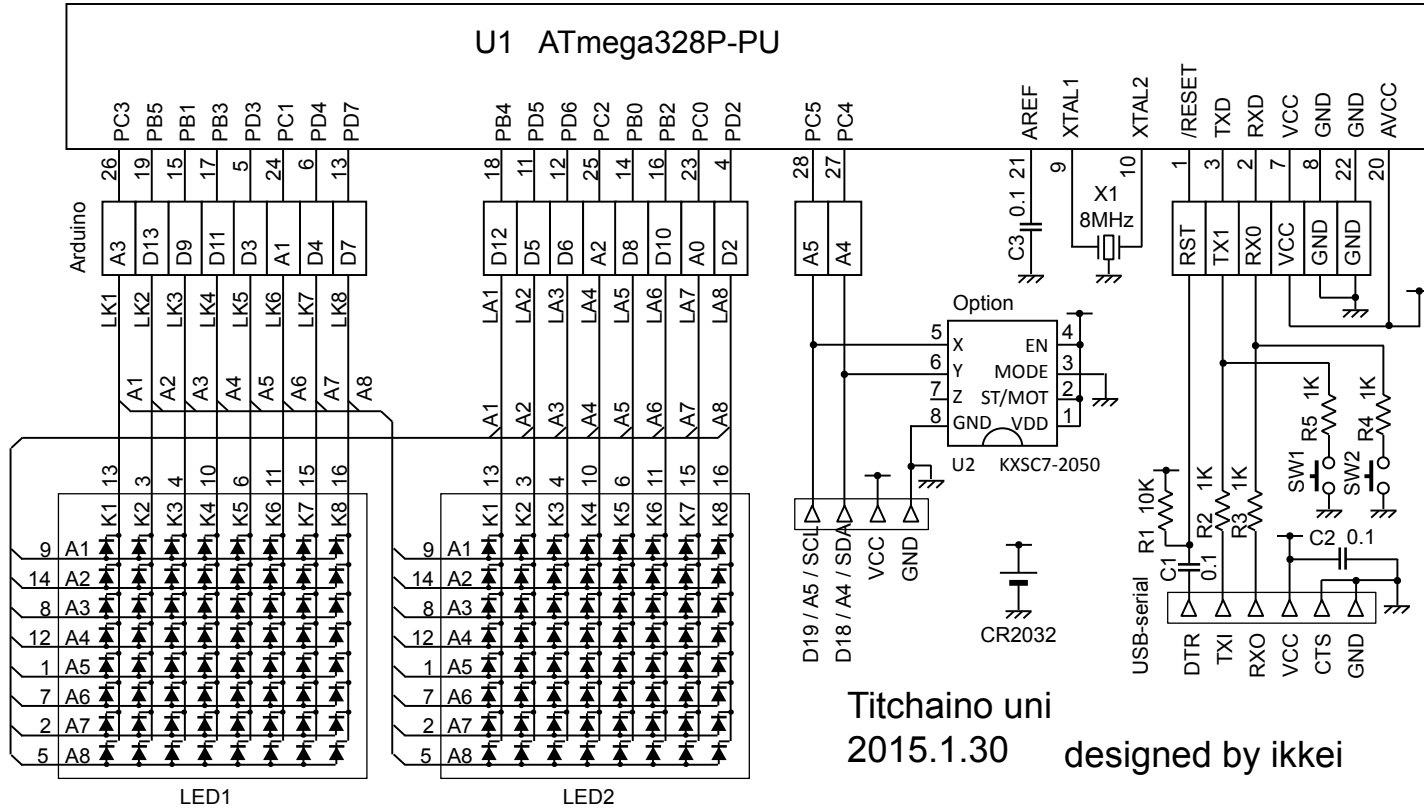
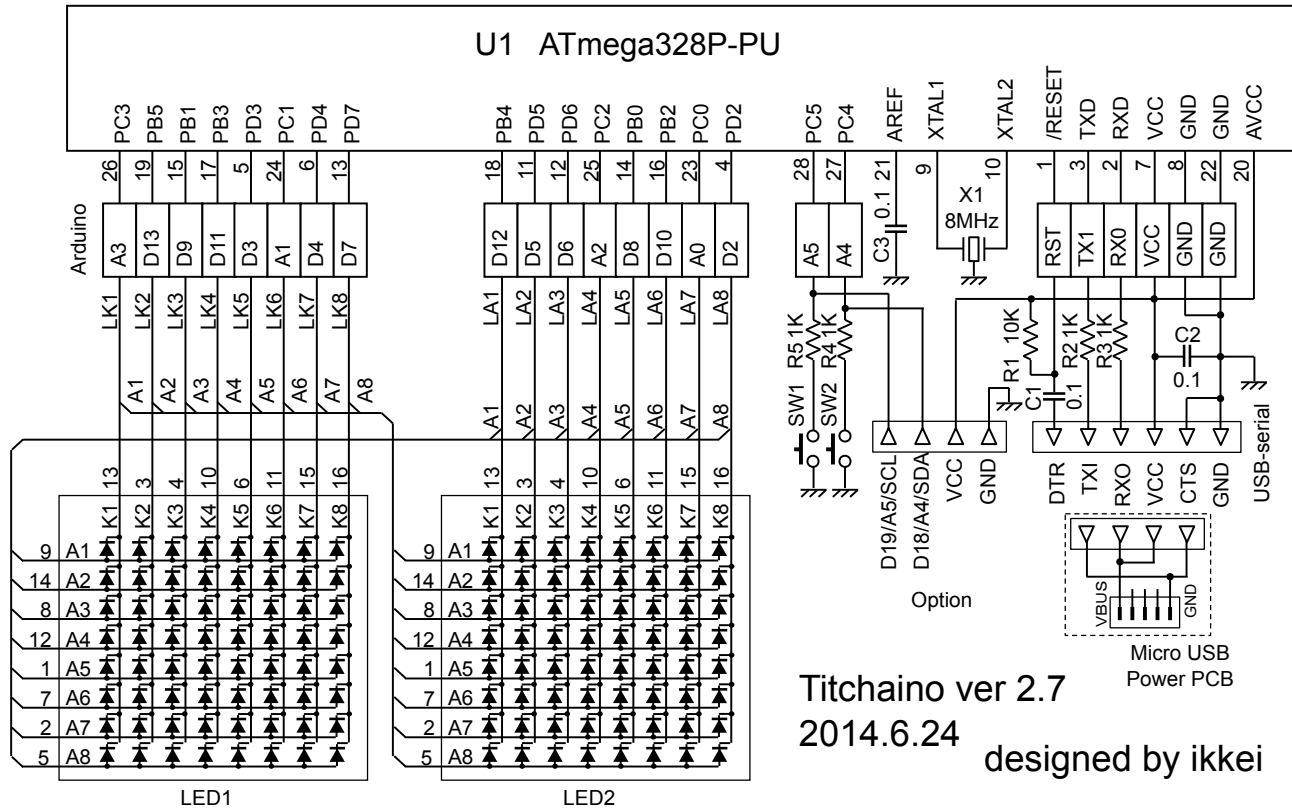


# Titchaino uni



Titchaino uni  
2015.1.30 designed by ikkei

# Titchaino ver 2.7

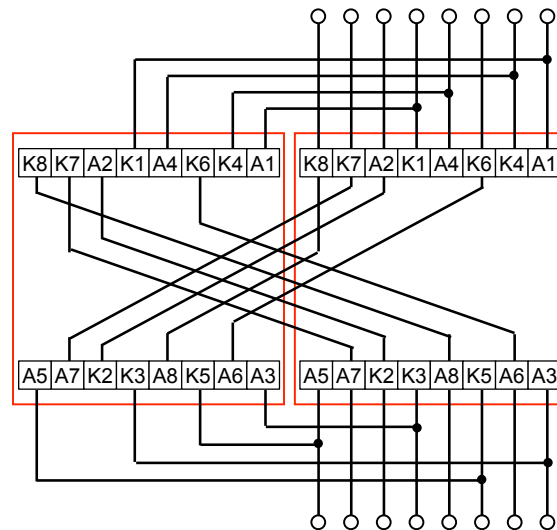
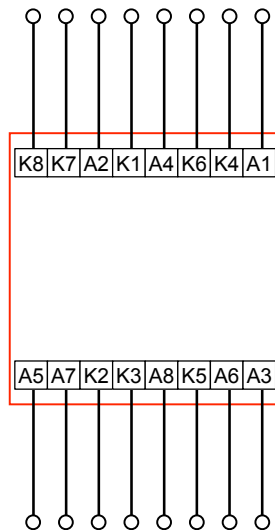
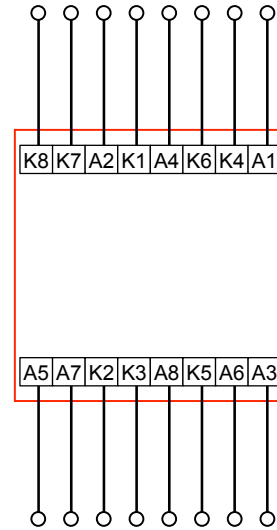


Titchaino ver 2.7

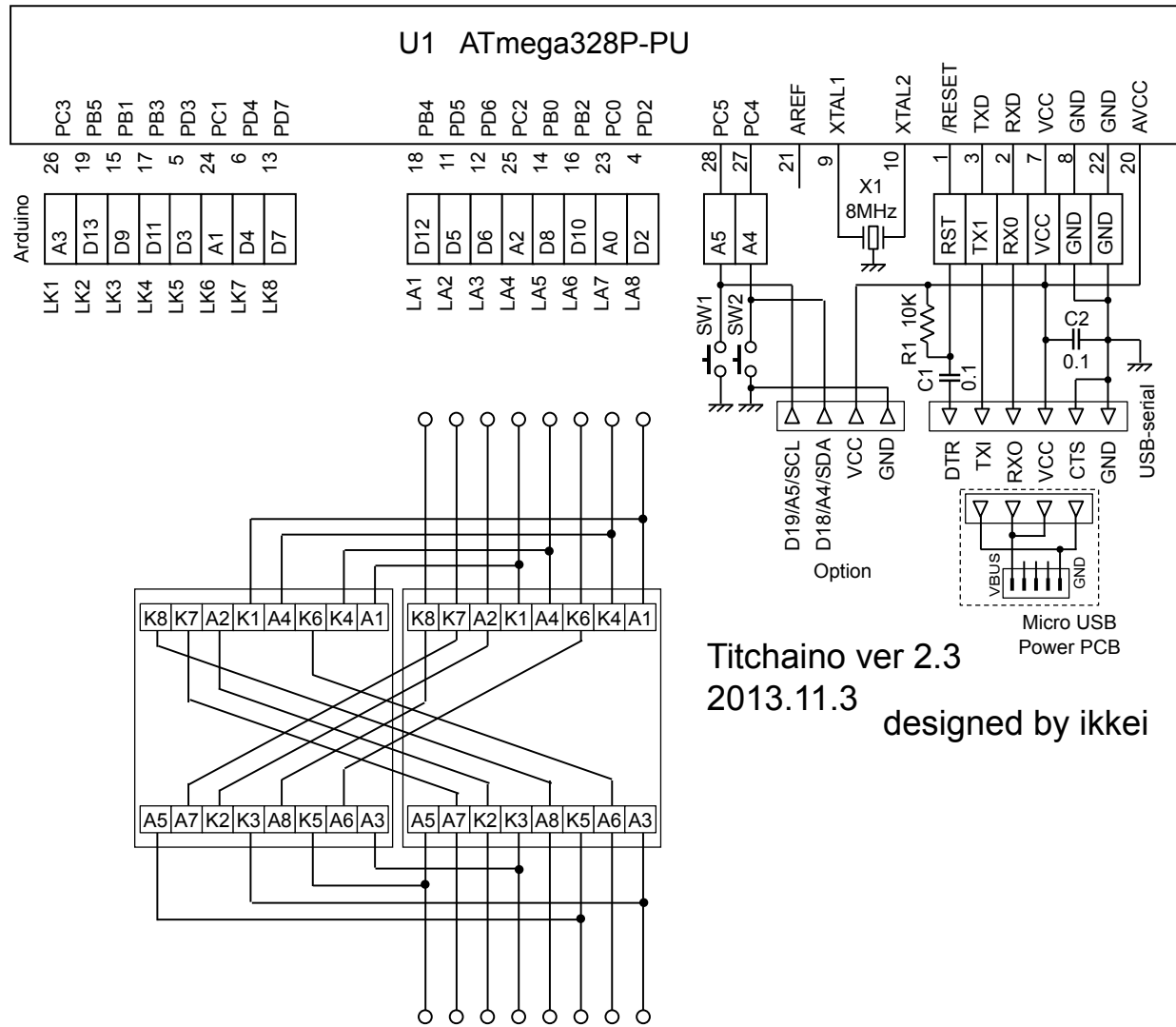
2014.6.24

designed by ikkei

# Titchaino ver 2.3



# Titchaino ver 2.3

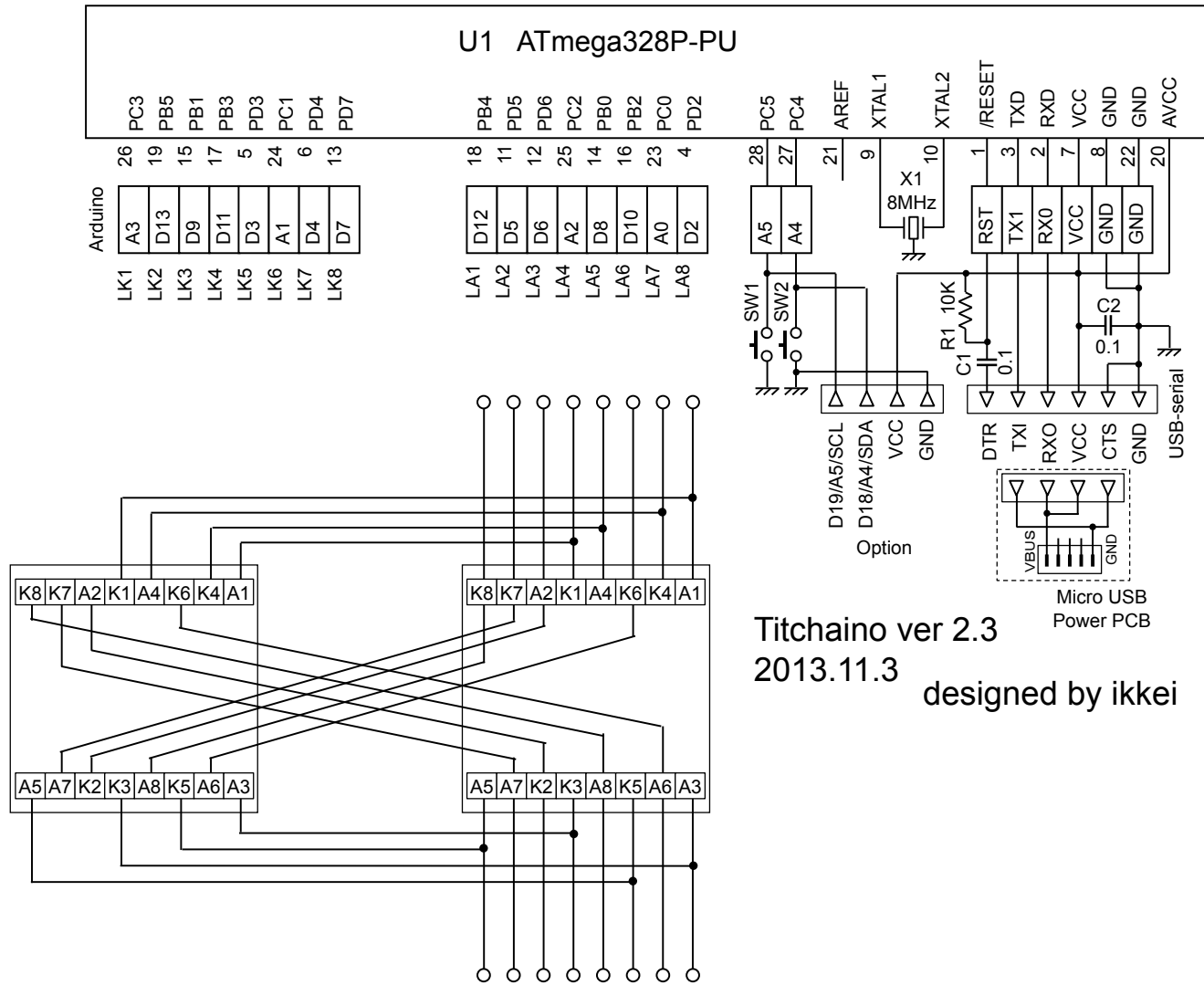


Titchaino ver 2.3

2013.11.3

designed by ikkei

# Titchaino ver 2.3

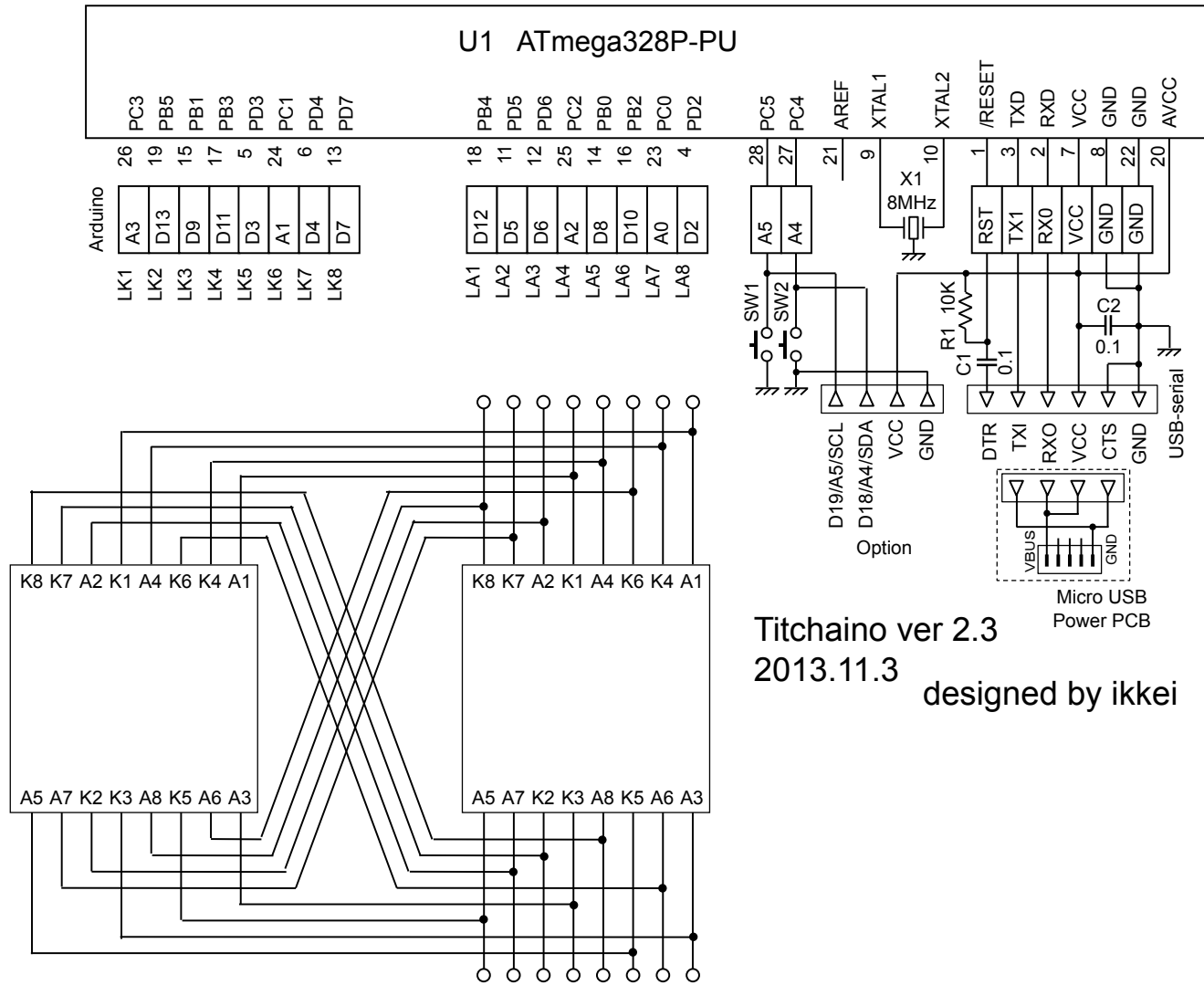


Titchaino ver 2.3

2013.11.3

designed by ikkei

# Titchaino ver 2.3

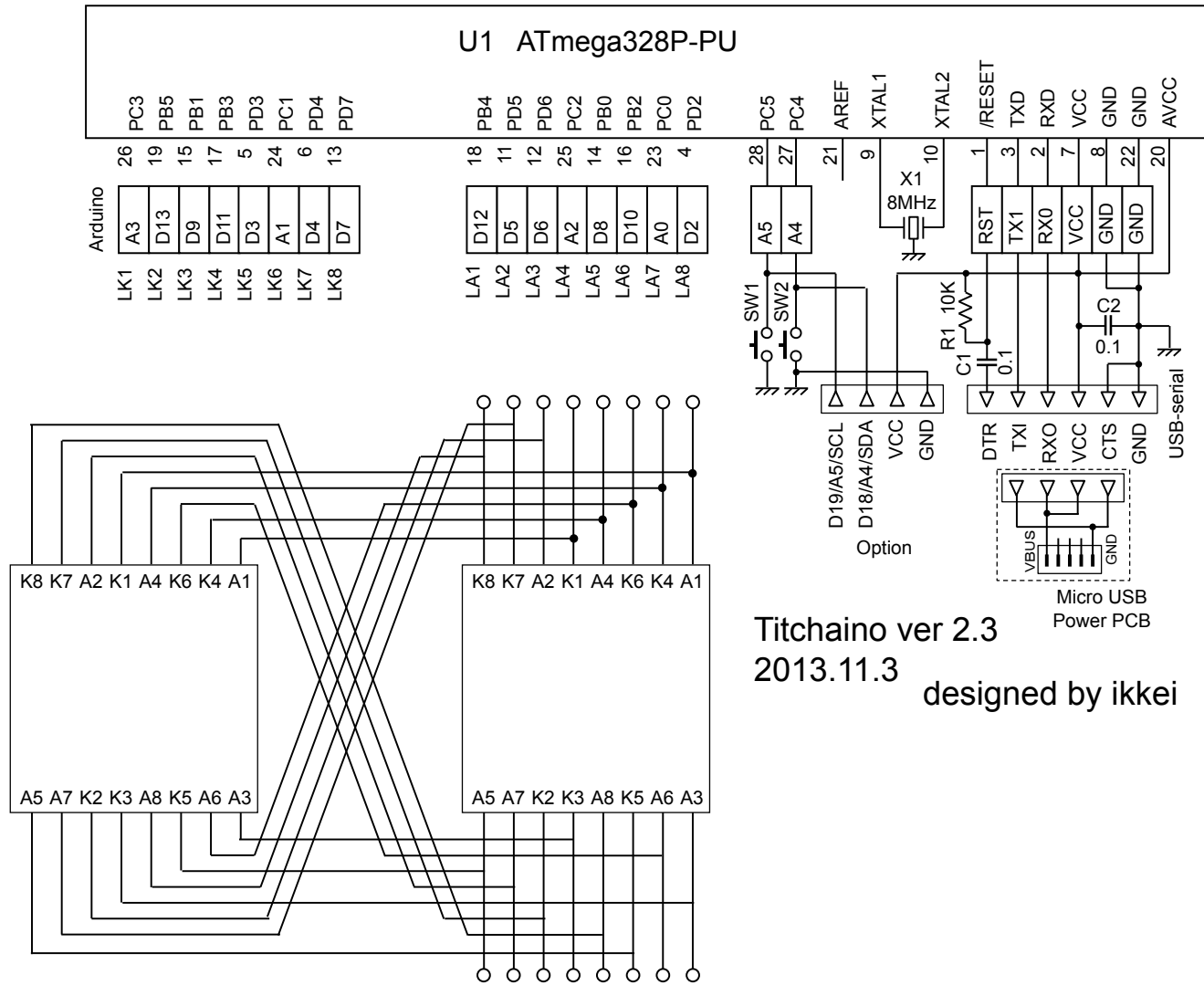


Titchaino ver 2.3

2013.11.3

designed by ikkei

# Titchaino ver 2.3

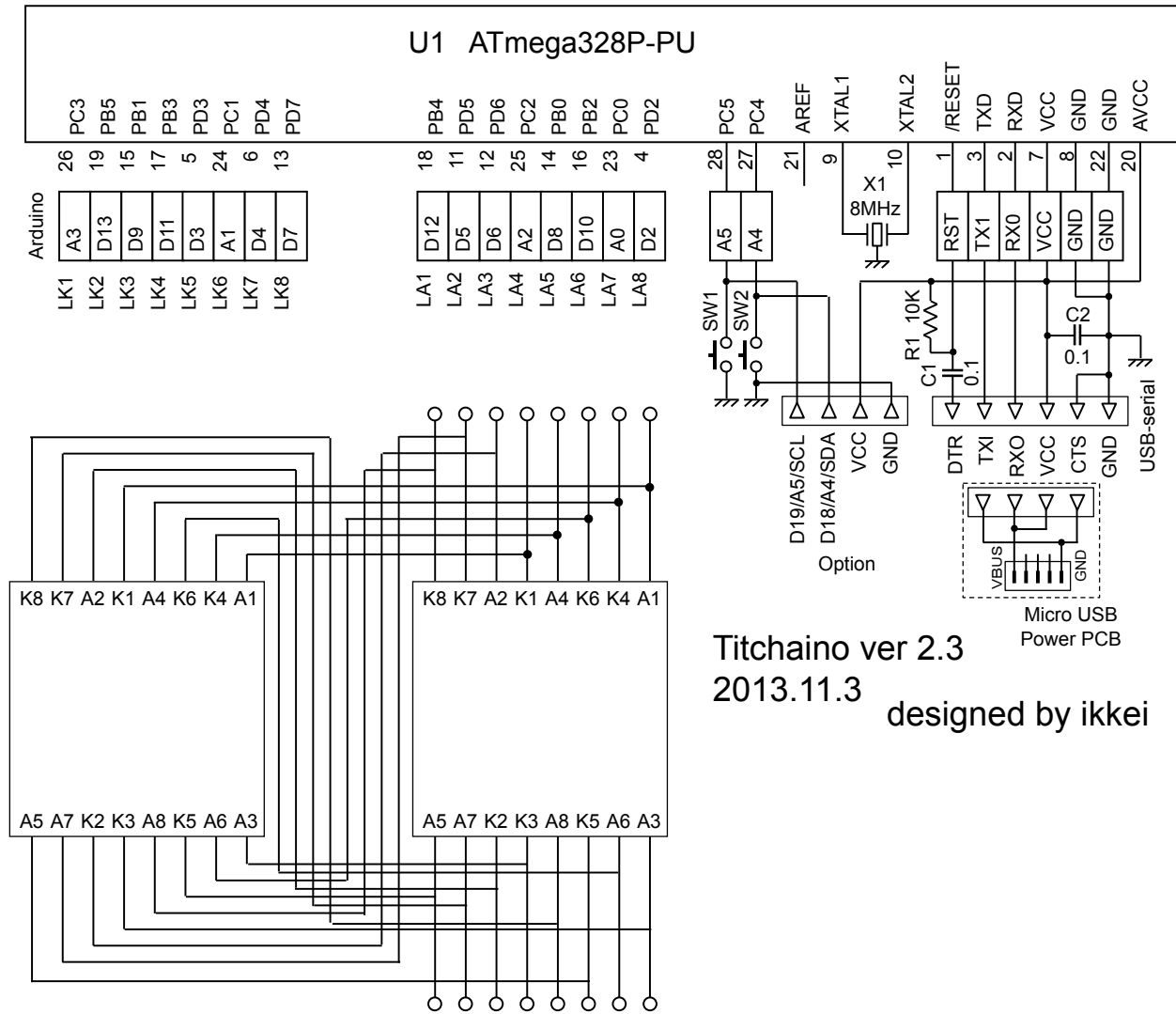


Titchaino ver 2.3

2013.11.3

designed by ikkei

# Titchaino ver 2.3

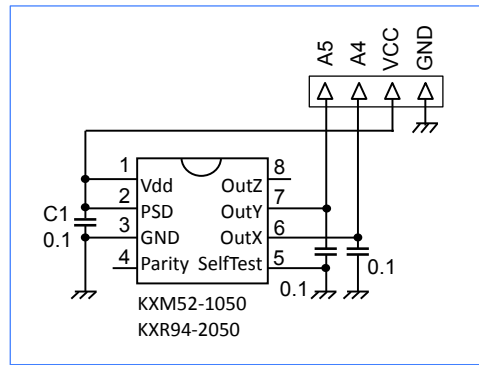
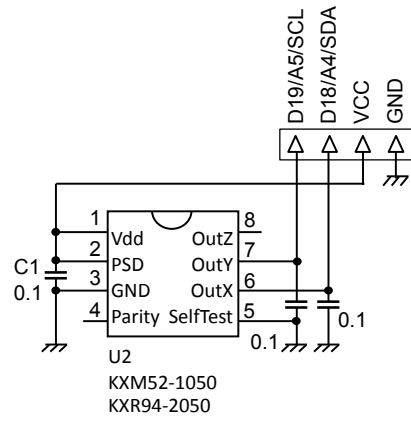


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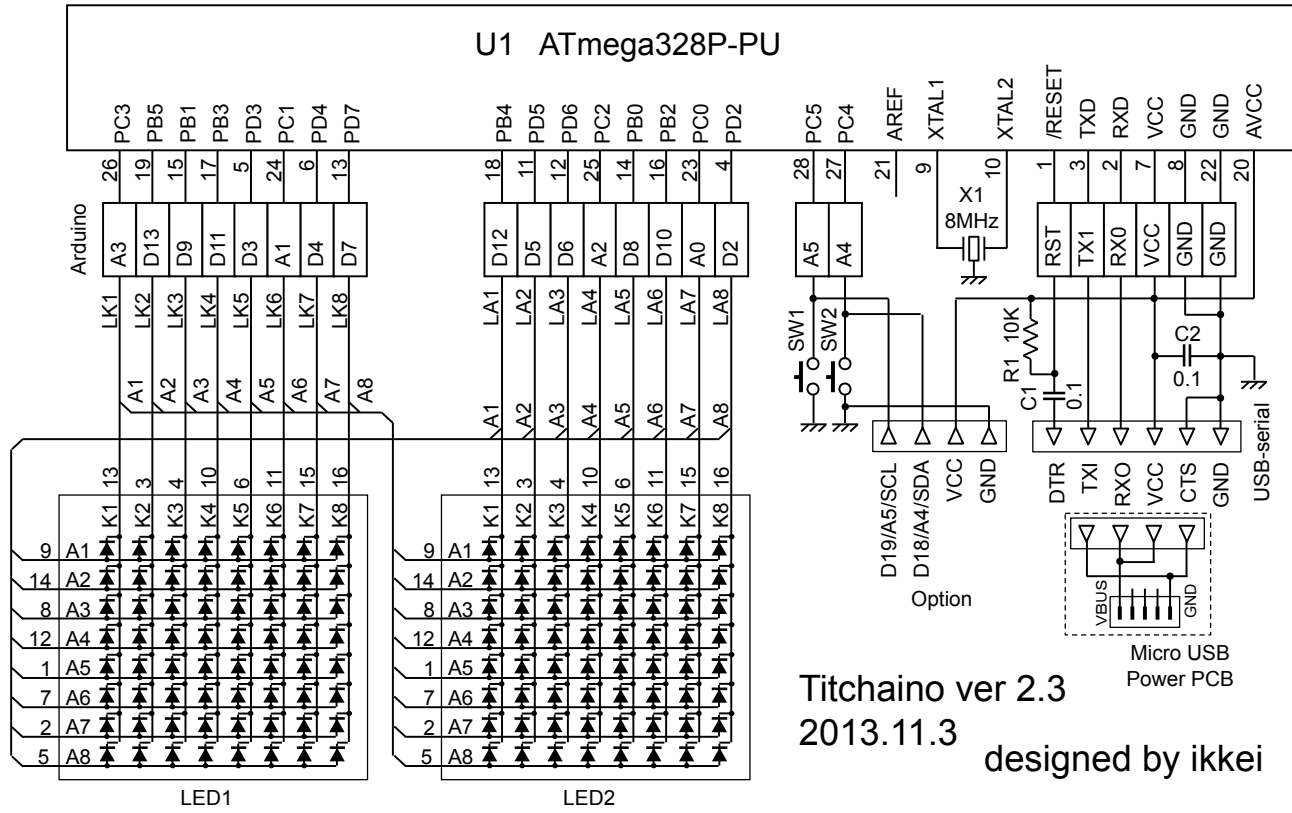
2013.11.3

designed by ikkei





# Titchaino ver 2.3

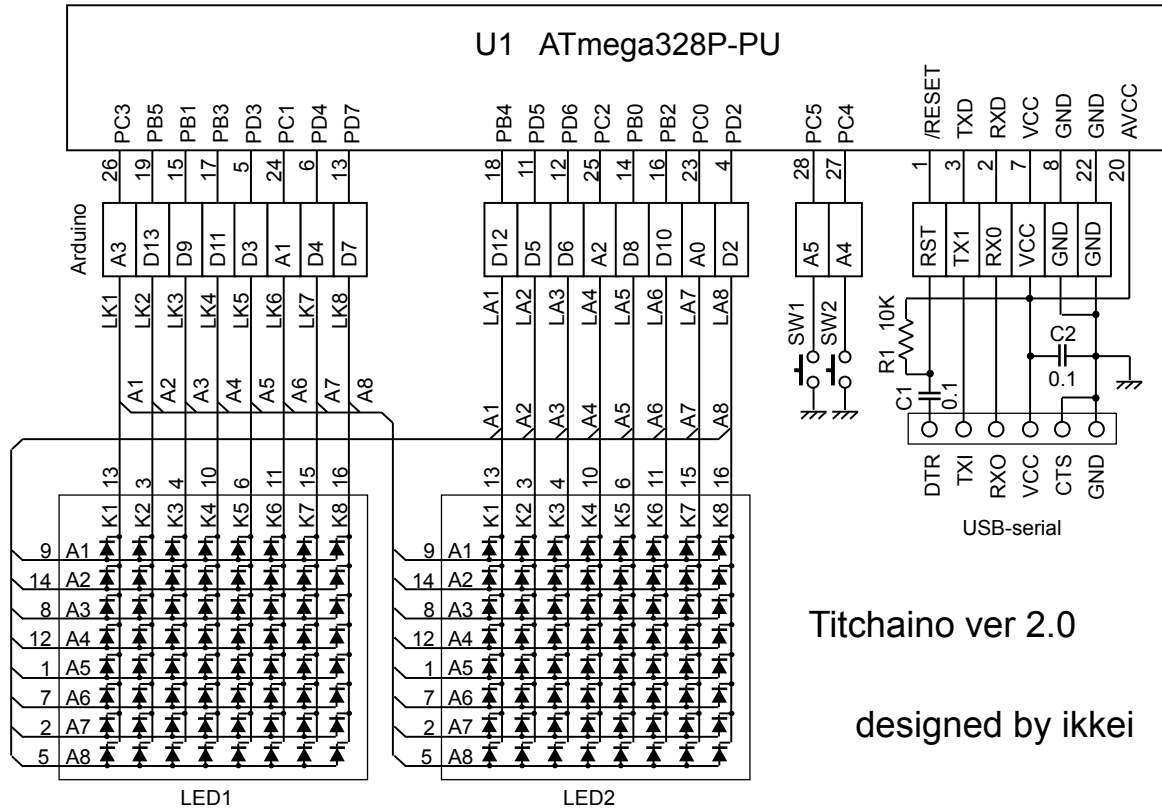


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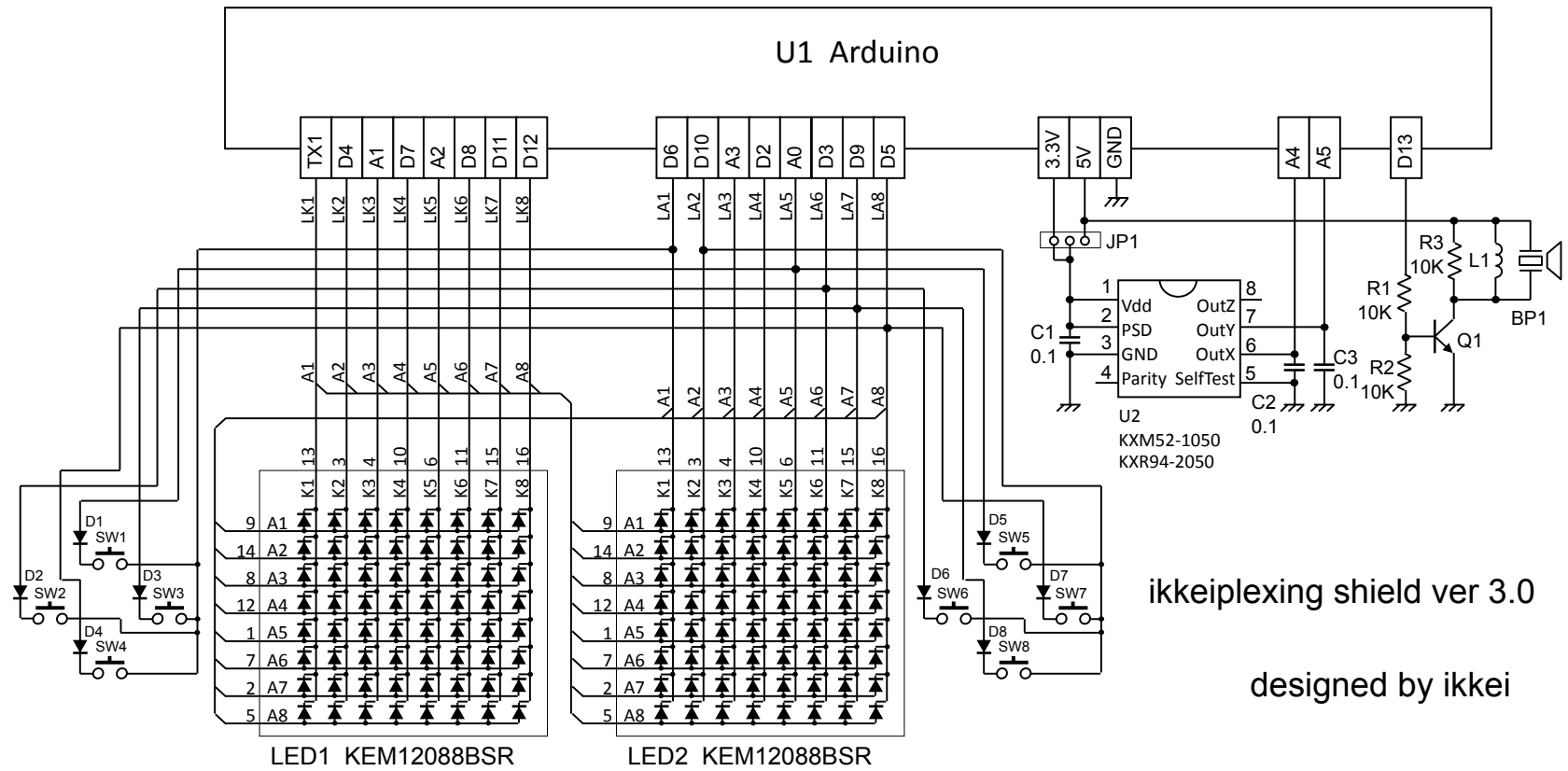
2013.11.3

designed by ikkei

# Titchaino ver 2.0



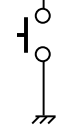
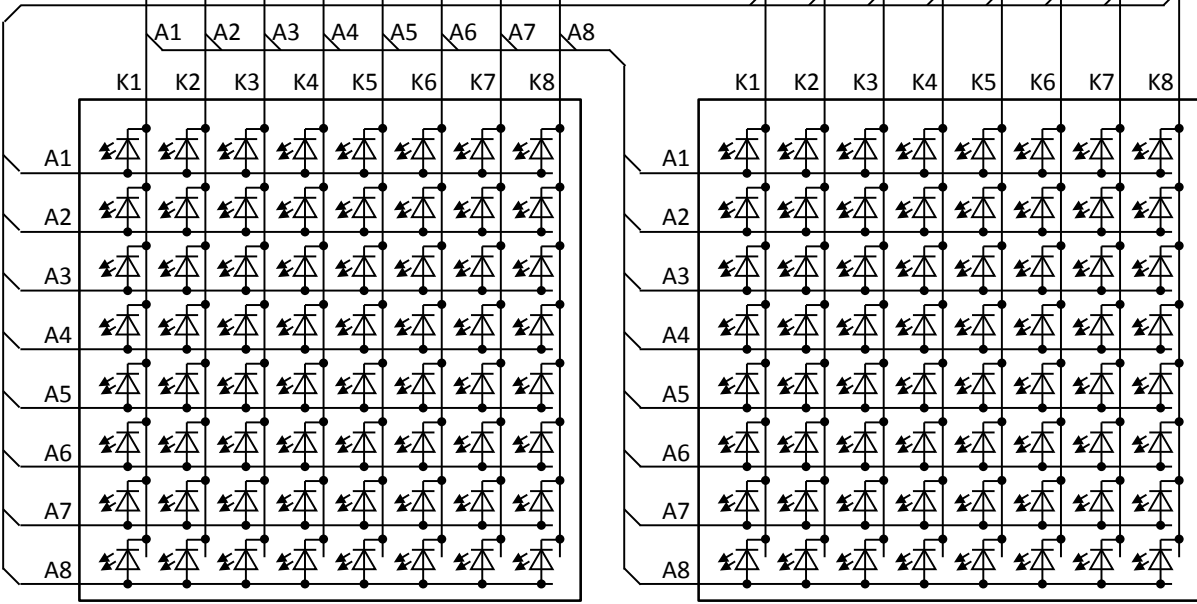
# ikkeplexing shield ver 3.0



# ATmega328P

A3 D13 D9 D11 D3 A1 D4 D7	D12 D5 D6 A2 D8 D10 A0 D2
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26 19 15 17 5 24 6 13	18 11 12 25 14 16 23 4
LK1 LK2 LK3 LK4 LK5 LK6 LK7 LK8	LA1 LA2 LA3 LA4 LA5 LA6 LA7 LA8



# ATmega328P

A3 D13 D9 D11 D3 A1 D4 D7 D12 D5 D6 A2 D8 D10 A0 D2  
PC3 PB5 PB1 PB3 PD3 PC1 PD4 PD7 PB4 PD5 PD6 PC2 PB0 PB2 PC0 PD2

26 19 15 17 5 24 6 13 18 11 12 25 14 16 23 4

LK1 LK2 LK3 LK4 LK5 LK6 LK7 LK8 LA1 LA2 LA3 LA4 LA5 LA6 LA7 LA8

A1 A2 A3 A4 A5 A6 A7 A8

